

Recardis *Solutions for IC Design Experts*

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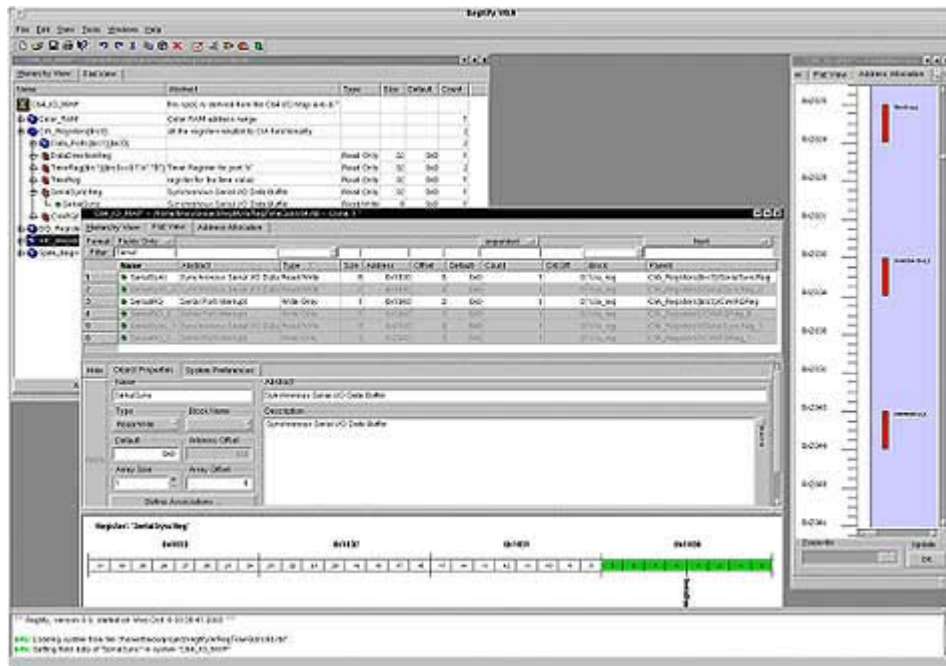
Press Release

Nuremberg / Germany : A team of experienced IC design engineers has joined forces under the brand name Recardis™ to create innovative design solutions for developers of complex integrated systems like ASICs, SOCs and FPGAs.

Regtify™- A Framework for Fast Realisation of Register Maps

On Date 04 Recardis will introduce its first commercially available product – Regtify– to the IC design community. Regtify is a flexible and highly customisable framework for management and automated implementation of register maps in large integrated systems, e.g. circuits for telecommunications and controllers.

Supported by a powerful GUI, system engineers efficiently define and inspect their systems' register structures. IC design engineers maintain their individual register implementation concepts and topologies based on VHDL components and templates. Regtify will use both system level information and hardware description to rapidly assemble and generate all register maps and accompanying documentation. Special purpose files, e.g. for software integration, will be generated based on user definitions.



Desktop-GUI featuring displaying various representations of register data.

Regtify is available for Solaris™ and Linux™. Windows™ support is planned in 2Q04. For more information visit us at Date Conference or contact:

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